

Technical Programme Topic Chairs

D1 System Specification and Modeling

Chair: Christian Haubelt

University of Rostock, DE

Co-Chair: Andy Pimentel

University of Amsterdam, NL

D2 System Design, High-Level Synthesis and Optimization

Chair: Andreas Herkersdorf

Technische Universität München, DE

Co-Chair: Nikil Dutt

University of California Irvine, US

D3 System Simulation and Validation

Chair: Prabhat Mishra

University of Florida, US

Co-Chair: Elena Vatajelu

Politecnico di Torino, IT

D4 Formal Methods and System Verification

Chair: Jason Baumgartner

IBM, US

Co-Chair: Julien Schmaltz

Eindhoven University of Technology, NL

DT5 Design and Test for Analog and Mixed-Signal Systems and Circuits

Chair: Günhan Dündar

Bogaziçi University, TR

Co-Chair: Haralampos Stratigopoulos

TIMA Laboratory (CNRS - Univ. Grenoble Alpes), FR

D6 Emerging Technologies and Systems

Chair: Michael Niemier

University of Notre Dame, US

Co-Chair: Ian O'Connor

University of Lyon, FR

D7 Power Modeling, Optimization and Low-Power Design

Chair: Martino Ruggiero

Wispes, IT

Co-Chair: Marisa López-Vallejo

Univ. Politecnica de Madrid, ES

D8 Network on Chip

Chair: Fabien Clermidy

CEA-LETI, FR

Co-Chair: Steven Nowick

Columbia, US

D9 Architectural and Microarchitectural Design

Chair: Todd Austin

University of Michigan, US

Co-Chair: Cristina Silvano

Politecnico di Milano, IT

D10 Temperature and Variability Aware Design and Optimization

Chair: Siddharth Garg

New York University, US

Co-Chair: Giovanni Ansaloni

EPFL, CH

D11 Reconfigurable Computing

Chair: Marco Platzner

University of Paderborn, DE

Co-Chair: Ryan Kastner

University of California San Diego, US

D12 Logic and Physical Synthesis, Timing Analysis and Verification

Chair: Jose Monteiro

INESC-ID / Técnico, Universidade de Lisboa, PT

Co-Chair: Patrick Groeneveld

Synopsys, US

D13 On-Chip and Off-Chip Parasitic Extraction, Model Order Reduction and Signal Integrity

Chair: Luca Daniel

Massachusetts Institute of Technology, US

Co-Chair: Luis Miguel Silveira

Technical University of Lisbon, PT

A1 Green Computing Systems

Chair: Qinru Qiu

, US

Co-Chair: Andreas P. Burg

EPFL, CH

A2 Communication, Consumer and Multimedia Systems

Chair: Theocharis Theocharides

University of Cyprus, CY

Co-Chair: Sergio Saponara

University of Pisa, IT

A3 Automotive Systems and Smart Energy Systems

Chair: Bart Vermeulen

NXP Semiconductors, NL

Co-Chair: David Boyle

Imperial College London, UK

A4 Ambient Intelligence and Ultra-Low Power Systems for Healthcare and Wellness

Chair: Srinivasan Murali

SmartCardia Särl, CH

Co-Chair: Elisabeta Farella

Fondazione Bruno Kessler, IT

A5 Secure Systems

Chair: Guido Bertoni

STMicroelectronics, IT

Co-Chair: Tim Güneysu

Ruhr University Bochum, DE

A6 Reliable and Reconfigurable Systems

Chair: Marco Domenico Santambrogio

Polimi, IT

Co-Chair: Praveen Raghavan

IMEC, BE

A7 Industrial Experiences Brief Papers

Chair: Ahmed Jerraya

CEA, FR

Co-Chair: Michael Nicolaidis

TIMA, Grenoble Institute of Technology, FR

T1 Defects, Faults, Variability and Reliability Analysis and Modeling

Chair: Rob Aitken

ARM, US

Co-Chair: Michel Renovell

LIRMM, FR

T2 Test Generation, Simulation and Diagnosis

Chair: Bernd Becker

Universitaet Freiburg, DE

Co-Chair: Jacob Abraham

UT Austin, US

T3 Design-for-Test, Test Compression and Access

Chair: Paolo Prinetto

Politecnico di Torino, IT

Co-Chair: Magdy Abadir

Independent, US

T4 On-Line Test, Fault Tolerance and Robust Systems

Chair: Fabrizio Lombardi

Northeastern University, US

Co-Chair: Cristiana Bolchini

Politecnico di Milano, IT

DT5 Design and Test for Analog and Mixed-Signal Systems and Circuits

Chair: Günhan Dündar

Bogaziçi University, TR

Co-Chair: Haralampos Stratigopoulos

TIMA Laboratory (CNRS - Univ. Grenoble Alpes), FR

E1 Real-time, Networked, and Dependable Systems

Chair: Iain Bate

University of York, UK

Co-Chair: Rodolfo Pellizzoni

University of Waterloo, CA

E2 Compilers for Embedded Systems

Chair: Alain Darte

University of Lyon, FR

Co-Chair: Rodric Rabbah

IBM, US

E3 Model-based Design and Verification for Embedded Systems

Chair: Saddek Bensalem

Université Joseph Fourier/Verimag, FR

Co-Chair: Linh Thi Xuan Phan

University of Pennsylvania, US

E4 Embedded Software Architectures

Chair: Marc Geilen

Eindhoven University of Technology, NL

Co-Chair: Frédéric Petrot

TIMA, Grenoble Institute of Technology, FR

E5 Cyber-Physical Systems

Chair: Rolf Ernst

Technical University of Braunschweig, DE

Co-Chair: Paul Pop

Technical University of Denmark, DK