

DATE Best Paper Awards

Each year the Design, Automation and Test in Europe Conference presents awards to the authors of the best papers. The selection is performed by an award committee, based on the results of the reviewing process and the quality of the final paper.

The DATE 2015 best papers are:

D Track

PHASE-NOC: TDM SCHEDULING AT THE VIRTUAL-CHANNEL LEVEL FOR EFFICIENT NETWORK TRAFFIC ISOLATION

*Anastasios Psarras, Ioannis Seitanidis and Giorgos Dimitrakopoulos,
Democritus University of Thrace, Greece
Chrysostomos Nicopoulos,
University of Cyprus, Cyprus*

A Track

HARDWARE TROJAN DETECTION FOR GATE-LEVEL ICS USING SIGNAL CORRELATION BASED CLUSTERING

*Burcin Cakir, Sharad Malik,
Princeton University USA*

T Track

DIGITAL CIRCUITS RELIABILITY WITH IN-SITU MONITORS IN 28NM FULLY DEPLETED SOI

*Marine Saliva, Florian Cacho, Vincent Huard, Xavier Federspiel, Damien Angot, Ahmed Benhassian,
STMicroelectronics, France;
Alain Bravaix,
IM2NP-ISEN, France;
Lorena Anghel,
TIMA, France*

Best Paper Award Nominations

D track

ASYMMETRIC UNDERLAPPED FINFET BASED ROBUST SRAM DESIGN AT 7NM NODE

*Arun Goud Akkala, Rangharajan Venkatesan, Anand Raghunathan, Kaushik Roy
Purdue University*

FAST EYE DIAGRAM ANALYSIS FOR HIGH-SPEED CMOS CIRCUITS

*Seyed Nematollah Ahmadyan, Shobha Vasudevan
University of Illinois at Urbana-Champaign
Eli Chiprout, Chenjie Gu, Suriyaprakash Natarajan
Intel*

A track

WORKLOAD UNCERTAINTY CHARACTERIZATION AND ADAPTIVE FREQUENCY SCALING FOR ENERGY MINIMIZATION OF EMBEDDED SYSTEMS

*Anup Das, Akash Kumar, Bharadwaj Veeravalli, Rishad Shafik, Geoff Merrett, Bashir Al-Hashimi
University of Southampton, National University of Singapore*

AN ULTRA-LOW POWER DUAL-MODE ECG MONITOR FOR HEALTHCARE AND WELLNESS

*Daniele Bortolotti, Mauro Mangia, Andrea Bartolini, Riccardo Rovatti, Gianluca Setti, Luca Benini
University of Bologna
University of Ferrara, ETH Zurich*

REDUCING TRACE SIZE IN MULTIMEDIA APPLICATIONS ENDURANCE TESTS

*Serge Vladimir Emteu Tchagou, Alexandre Termier, Jean-François Méhaut, Brice Videau, Miguel Santana, René Quiniou
University of Grenoble Alpes, STMicroelectronics, University of Rennes, INRIA Rennes*

T track

GPU-ACCELERATED SMALL DELAY FAULT SIMULATION

*Eric Schneider, Stefan Holst, Michael Kochte, Xiaoqing Wen, Hans-Joachim Wunderlich
University of Stuttgart, Kyushu Institute of Technology*