The ACM SIGDA / EDAA PhD forum is part of the DATE Conference and hosted by ACM SIGDA and the European Design Automation Association (EDAA). It offers the opportunity for PhD students to present their thesis work to a broad audience in the design, automation and test community from academia and industry. During the presentation at the DATE Conference, it helps students to establish contacts. Also, representatives from industry and academia get a glance of state-of-the-art research in design, automation and test. The review process resulted in the selection of the PhD students listed below. We thank ACM SIGDA, EDAA, and DATE for making this Forum possible.

*Peter Marwedel (Chair, ACM SIGDA / EDAA PhD Forum at DATE 2014)*

**PhD Forum Committee**

Peter Marwedel (Chair), TU Dortmund, Germany  
Walter Anheier, University of Bremen, Germany  
Davide Bertozzi, Università degli studi di Ferrara, Italy  
Georgio Di Natale, LIRMM, Montpellier, France  
Michael Engel, TU Dortmund, Germany  
Joan Figueras, Universitat Politècnica de Catalunya, Barcelona, Spain  
Helmut Graeb, TU München, Germany  
Younghyun Kim, Purdue University, West Lafayette, USA  
Zhuo Li, IBM, Austin, USA  
Tulika Mitra, National University of Singapore, Singapore  
Gi-Joon Nam, IBM, Austin, USA  
Ulrich Rückert, Bielefeld University, Germany  
Sander Stuijk, TU Eindhoven, Netherlands  
Miroslav Velev, Aries Design Automation, Chicago, USA  
Norbert Wehn, TU Kaiserslautern, Germany

**Admitted Presentations**

1. Agathoklis Papadopoulos (University of Cyprus, CY): Accelerating Bioinformatics and Biomedical Applications via Massively Parallel Reconfigurable Systems  
4. Anup Das (National University of Singapore, SG): Design Methodology for Reliable and Energy Efficient Multiprocessor Systems  
5. Arnaldo Cruz (Kyushu University, JP): Compiler optimization space exploration using machine learning techniques  
7. Daniele Bortolotti (University of Bologna, I): A Process and Environmental Variation Tolerance Scheme for ULP Shared-memory Processor Cluster  
9. Domitian Tamas-Selicean (Technical University of Denmark, DK): Design of Mixed-Criticality Applications on Distributed Real-Time Systems  
10. Emad Ebeid (University of Verona, I): Modeling and Synthesis of the Network in Distributed Embedded Systems
11. Fabian Oboril (Karlsruhe Institute of Technology, D): Cross-Layer Approaches for Aging-Aware Design of Nanoscale Microprocessors
12. Fatemeh Negin Javaheiri (TIMA Lab, F): Designing from Assertions: from PSL Properties to a Compliant Hardware Prototype
13. Fazal Hameed (Karlsruhe Institute of Technology, D): DRAM Aware Last Level Cache Policies for Multi-Core Systems
14. Francesco Conti (University of Bologna, I): Heterogeneity exploration in tightly-coupled clusters
16. Karthik Chandrasekar (Delft University of Technology, NL): High-Level Power Estimation of DRAMs
17. Lars Middendorf (University of Rostock, D): Dynamic Task Mapping on Multi-Core Architectures using Stream Rewriting
18. Leonidas Kosmidis (Barcelona Supercomputing Center, ES): Enabling Caches in Probabilistic Timing Analysis
19. Luca Cassano (University of Pisa, I): Analysis and Test of the Effects of Single Event Upsets Affecting the Configuration Memory of SRAM-based FPGAs
20. Marco Indaco (Politecnico di Torino, I): Service Oriented Non Volatile Memories
22. Mathias Soeken (University of Bremen, D): Formal Specification Level
23. Milan Pavlovic (Barcelona Supercomputing Center, ES): Data Placement in HPC Architectures with Heterogeneous Off-chip Memory
25. Miroslav Valka (University of Montpellier II, F): Power Aware Test and Test of Low Power Devices
26. Mohamed Bamakhrama (Leiden University, NL): On Hard Real-Time Scheduling of Cyclo-Static Dataflow and its Application in System-Level Design
27. Namita Sharma (Indian Institute of Technology Delhi, IN): Data Memory Optimizations for SPM based Baseband Processor Architectures
28. Nikola Rajovic (Barcelona Supercomputing Center, ES): High Performance Computing with Mobile SoCs: Opportunities and Challenges
29. Norma Montealegre (Heinz Nixdorf Institut, Paderborn, D): Immunorepairing of Hardware Systems
30. Ogun Turkyilmaz (CEA-LETI, Grenoble, F): Using 3D technologies to reduce power consumption of FPGAs
31. Oliver Arnold (TU Dresden, D): Dynamic Task Scheduling for heterogeneous MPSoCs
32. Pydi Bahubalindruni (University of Porto, PT): Analog/Mixed – Signal Circuits using a-GIZO TFTs
34. Saman Kiamehr (Karlsruhe Institute of Technology, D): Cross layer resiliency modeling and optimization: A device to circuit approach
35. Samaneh Ghandali (University of Tehran, IR): High-level Synthesis and Optimization of Datapath-intensive Embedded System Designs
36. Sudip Roy (Indian Institute of Technology Kharagpur, IN): Algorithms for Design Automation of Sample Preparation on Digital Microfluidic Biochips
37. Turbo Majumder (Indian Institute of Technology Delhi, IN): On-Chip Network-Enabled Many-Core Architectures for Computational Biology Applications
38. Vito Giovanni Castellana (Politecnico di Milano, I): C-Based High Level Synthesis of Adaptive Hardware Components