CALL FOR PAPERS

- PhD Forum

Any submission not in line with the above rules will be rejected. The DATE conference will work cooperatively with others to check for double-submissions. It is mandatory that authors of accepted presentations attend to present their work at the conference and also that each paper is accompanied by at least one full conference registration fee payment at the speaker rate.

Special Sessions can take the form of Panels discussing visionary and/or controversial issues, Hot-Topic Sessions, dealing with the introduction and discussion of new R&D problems, and Embedded Tutorial Sessions, addressing trends in the technical domains that are of interest to the conference participants. Special Session proposals consist of an extended summary of up to 1500 words describing the session content and format, speakers/panellists, and title of their presentation. Proposals must be submitted via the DATE website by Sept. 13, 2013. Contributors of accepted special sessions will be asked to submit final texts or presentations of panels, as appropriate, for publication in the Proceedings (deadline Dec. 15, 2013). Panel sessions are entitled to one page per panellist in the Proceedings, whereas Embedded Tutorials and Hot Topic sessions are allocated a maximum of 6 pages per speaker. For more information, please contact:

Dmitri Hammami and Wolfgang Mueller
specialsessions@date-conference.com

INSTRUCTIONS

The accepted file format is PDF. Any other format and manuscripts received in hard-copy form will not be processed. Papers can be submitted for either formal oral presentation or for interactive presentation. Oral presentations require novel and complete research work supported by experimental results. Interactive presentations are expected to articulate emerging and future design, verification and test problems including work in progress and identify open problems that merit innovative future research. These presentations are given on a laptop in a face-to-face discussion area.

Submissions should not exceed 6 pages in length for oral-presentation papers and 4 pages in length for interactive-presentation papers. Submissions for Hot Topic and Test sessions are allocated a maximum of 6 pages per speaker. For additional information, please contact:

Peter Marwedel
peter.marwedel@tu-dortmund.de

An open discussion forum in the midst of Europe's largest electronic system design exhibition: The programme combines expert panel sessions and customers' testimonials. The aim is to offer information and inspiration to freshen up the key business and industrial issues in the electronic systems design marketplace. Submissions are invited from industry consortia, technology groups, standards bodies, or any person with opinions on future industry trends.

Delegates are invited to demonstrate their mature prototypes and pre-commercial information, please contact:

Franco Fummi franco.fummi@univr.it

PhD Forum Deadline: Nov. 8, 2013

Notification of acceptance Nov. 17, 2013

Camera-ready paper due Date: Dec. 19, 2013

University Booth proposals Deadline: Jan. 19, 2014

To permit blind review, submissions should not include author names or institutions. All papers should be formatted as close as possible to the final format: At least one copy of A4 or letter sheets, double column, single spaced. Times or equivalent font of minimum 10pt (templates are available on the DATE Web site for your convenience). A double blind review, submission should not include the author names. Any submission not in line with the above rules will be discarded. All papers will be evaluated with regard to their suitability for the conference, originality, and technical soundness. The Programme committee reserves the right to request oral-presentation papers to interactive presentation and vice versa, to obtain the most suitable presentation format. Submissions simultaneously under review or accepted by another conference, symposium, or journal will be rejected. The DATE conference will work cooperatively with others to check for double-submissions. It is mandatory that authors of accepted presentations attend to present their work at the conference and also that each paper is accompanied by at least one full conference registration fee payment at the speaker rate.
The conference addresses all aspects of research into technologies for electronic and (embedded) systems engineering. It covers the design process, test, and tools for design automation of electronic products ranging from integrated circuits to distributed large-scale systems. This includes both hardware and embedded software design issues. The conference scope also includes the selection of design requirements and new architectures for challenging application fields such as telecom, wireless communications, multimedia, healthcare and automotive systems. Papers invited in innovative industrial designs or research with an industrial motivation are particularly encouraged to submit papers to foster the feedback from design to research. Panels, Hot-Topics and Embedded tutorials highlight and inform about challenging topics.

Special Days in the programme will focus on two areas bringing new aspects to the DATE conference: Advancing Electronics beyond CMOS and the System-Area approach.

**Advancing Electronics beyond CMOS**

As the issues associated with CMOS scaling become harder and more costly to solve, this special day will cover the latest trends towards alternative devices and paradigms for computing and data acquisition, storage and transport. While the potential for innovation is tremendous, the main challenge is to understand how to choose from such a broad spectrum of approaches. Important topics include nanoscale switching devices and computing nanofabrics, trends in memory technologies in hybrid 3D integration, flexible and organic electronics, new state variable vectors (spintronics, photonics) and interfaces to the natural world (sensor networks, data analysis, defense, bioMEMS).

**System-Area Design**

To reflect current industrial practices as well as previsions of future design trends, in System-Area design, a part of the main conference, the System-Level Design track will cover the latest trends towards alternative devices and paradigms for computing and data acquisition, storage and transport. While the potential for innovation is tremendous, the main challenge is to understand how to choose from such a broad spectrum of approaches. Important topics include nanoscale switching devices and computing nanofabrics, trends in memory technologies in hybrid 3D integration, flexible and organic electronics, new state variable vectors (spintronics, photonics) and interfaces to the natural world (sensor networks, data analysis, defense, bioMEMS).