

KEY DATES submission key dates

D, A, T and E papers Deadline: Sept. 13, 2013

Special Session proposals Deadline: Sept. 13, 2013

Tutorial proposals Deadline: Sept. 13, 2013

Friday Workshop proposals Deadline: Sept. 13, 2013

Exhibition Theatre Deadline: Oct. 21, 2013

PhD Forum Deadline: Nov. 8, 2013

Notification of acceptance Nov. 17, 2013

Camera-ready paper due date Deadline: Dec. 15, 2013

University Booth proposals Deadline: Jan. 19, 2014

INSTRUCTIONS submission instructions

All manuscripts for any technical topic of the D, A, T, and E tracks must be submitted for review electronically, following the instructions on the conference website:

www.date-conference.com

The accepted file format is PDF. Any other format and manuscripts received in hard-copy form will not be processed.

Papers can be submitted for either formal oral presentation or for interactive presentation. Oral presentations require novel and complete research work supported by experimental results. Interactive presentations are expected to articulate emerging and future design, verification and test problems including work in progress and identify open problems that merit innovative future research. These presentations are given on a laptop in a face-to-face discussion area.

Submissions should not exceed 6 pages in length for oral-presentation papers and 4 pages in length for interactive-presentation papers. Submission of two-page papers in topic A7 (Industrial Experiences Brief Papers) will also be considered.

All papers should be formatted as close as possible to the final format: A4 or letter sheets, double column, single spaced, Times or equivalent font of minimum 10pt (templates are available on the DATE Web site for your convenience). *To permit blind review, submissions should not include the author names.* Any submission not in line with the above rules will be discarded.

All papers will be evaluated with regard to their suitability for the conference, originality, and technical soundness. The Programme Committee reserves the right to reorient oral-presentation papers to interactive-presentation and vice versa, to obtain the most suitable presentation

format. Submissions simultaneously under review or accepted by another conference, symposium, or journal will be rejected. The DATE conference will work cooperatively with others to check for double-submissions. It is mandatory that authors of accepted presentations attend to present their work at the conference and also that each paper is accompanied by at least one full conference registration fee payment at the speaker rate.

SPECIAL SESSIONS special sessions

Special Sessions can take the form of **Panels** discussing visionary and/or controversial issues, **Hot-Topic Sessions**, dealing with the introduction and discussion of new R&D problems, and **Embedded Tutorial Sessions**, addressing trends in the technical domains that are of interest to the conference participants.

Special Session proposals consist of an extended summary of up to 1500 words describing the session content and format, speakers/panellists, and title of their presentation. Proposals must be submitted via the DATE website by **Sept. 13, 2013**.

Contributors of accepted special sessions will be asked to submit final texts or statements of panellists, as appropriate, for publication in the Proceedings (deadline **Dec. 15, 2013**). Panel sessions are entitled to one page per panellist in the Proceedings, whereas Embedded Tutorials and Hot Topic sessions are allocated a maximum of 6 pages per speaker. For more information, please contact:

Omar Hammami and Wolfgang Mueller
specialsessions@date-conference.com

TUTORIALS tutorials

On the first day, half-day technical tutorials on selected topics will be given by leading experts in their respective fields. The proposal should motivate the topic and its relevance for both academia and industry, describe the target audience (who? how many? match with typical DATE audience?), the reason for a tutorial (why a tutorial, why now), tutorial objectives, and a clear planning hereto (tutorial outline and speakers, what will be done to attract the audience). Proposals should be submitted before **Sept. 13, 2013** via the DATE website. For more information, please contact:

Franco Fummi
franco.fummi@univr.it

UNIVERSITY BOOTH university booth

The University Booth fosters the transfer of academic work to industry. The University Booth is part of the DATE exhibition and is free of charge for presenters and their visitors. Research institutes and universities interested in demonstrating their mature prototypes and pre-commercial results should contact by **Jan. 19, 2014**:

Jens Lienig and Andreas Voerg
university-booth@date-conference.com

PHD FORUM phd forum

The PhD Forum is a poster session hosted by the European Design and Automation Association (EDAA) and ACM-SIGDA for PhD students to present and discuss their research work with people in the design automation and system design community. It represents a good opportunity for students to get feedback on their research and for the industry to see academic work in progress. Submissions should be sent by **Nov. 8, 2013** to:

Peter Marwedel
peter.marwedel@tu-dortmund.de

THEATRE exhibition theatre

An open discussion forum in the midst of Europe's largest electronic system design exhibition: The programme combines expert panel sessions and customers' testimonials. The aim is to offer exhibition visitors a fresh view on the key industrial and business issues in the electronic systems design market. Submissions are invited from industry consortia, technology groups, standards bodies, or any person with opinions on future industry trends by **Oct. 21, 2013**:

Juergen Haase
haase@edacentrum.de

WORKSHOPS friday workshops

Proposals for Friday Workshops on emerging research and application topics are invited. Particularly suitable topics are those not directly covered in the technical programme and which have the potential to impact on future DATE technical areas in design and test of microelectronic systems. For information and detailed descriptions on how to propose a workshop, please refer to the Web site. Proposals should be submitted before **Sept. 13, 2013** via the DATE website. For more information, please contact:

Lorena Anghel and Cristiana Bolchini
fridayworkshops@date-conference.com

FRINGE MEETINGS fringe meetings

A limited number of rooms is available for the organisation of fringe meetings (birds of a feather sessions) open to all DATE attendees, as well as for appropriate on-site closed meetings. Requests are considered in the order of their arrival, and should be directed to:

Franziska Kremling
K.I.T. Group GmbH
Münzgasse 2,
01067 Dresden, Germany
kremling@kitdresden.de
Tel: + 49 351 4967 541
Fax: + 49 351 4956 116

Requests for commercial meetings will incur the standard room charges.

final call for papers

DATE 14

Design, Automation
and Test in Europe

Conference and Exhibition 2014
ICC, Dresden, Germany
March 24-28, 2014

www.date-conference.com

The 17th **DATE conference and exhibition** is the main European event bringing together designers and design automation users, researchers and vendors, as well as specialists in the hardware and software design, test and manufacturing of electronic circuits and systems. It puts strong emphasis on ICs/SoCs, reconfigurable hardware and embedded systems, including embedded software.

The five-day event consists of a **conference** with plenary invited papers, regular papers, panels, hot-topic sessions, tutorials and workshops, two special focus days and a track for executives. The scientific conference is complemented by a commercial **exhibition** showing the state-of-the-art in design and test tools, methodologies, IP and design services, reconfigurable and other hardware platforms, embedded software, and (industrial) design experiences from different application domains, e.g. automotive, wireless, telecom and multimedia applications. The organisation of user group meetings, fringe meetings, a university booth, a PhD forum, vendor presentations and social events offers a wide variety of extra opportunities to meet and exchange information on relevant issues for the design and test community. Special space will also be allocated for EU-funded projects to show their results.

THE CONFERENCE the conference

The **conference** addresses all aspects of research into technologies for electronic and (embedded) systems engineering. It covers the design process, test, and tools for design automation of electronic products ranging from integrated circuits to distributed large-scale systems. This includes both hardware and embedded software design issues. The conference scope also includes the elaboration of design requirements and new architectures for challenging application fields such as telecom, wireless communications, multimedia, healthcare and automotive systems. Persons involved in innovative industrial designs are particularly encouraged to submit papers to foster the feedback from design to research. Panels, hot-topic sessions and embedded tutorials highlight and inform about emerging topics.

Special Days in the programme will focus on two areas bringing new challenges to the system design community:

Advancing Electronics beyond CMOS As the issues associated with CMOS scaling become harder and more costly to solve, this special day will cover the latest trends towards alternative devices and paradigms for computing and data acquisition, storage and transport. While the potential for innovation is tremendous, the main challenge is to understand how to choose from such a broad spectrum of approaches. Important topics include nanoscale switching devices and computing nanofabrics, trends in memory technologies in hybrid 3D integration, flexible and organic electronics, new state variable vectors (spintronics, photonics) and interfaces to the natural world (sensor networks, data analysis, displays, MEMS).

System-Level Design to reflect current industrial practices as well as present recent advances in System-Level Design research. A particular emphasis will be on ultra-low power design and modeling at multiple abstraction levels, virtual platforms for software development and architecture design of MPSoCs and system integration through cosimulation. We will also take a look at high-level synthesis from different input languages as well as

software code generation techniques. Important topics also include multi-core enablement for safety-critical and real-time embedded systems as well as accelerator-rich design for the fight against dark silicon.

On the first day of the DATE event, half-day in-depth technical **tutorials** are given by leading experts in their respective fields. The tutorials are well suited for researchers, tool developers and system designers.

Friday Workshops concentrate on specialised and novel topics.

THE EXHIBITION the exhibition

The conference is complemented by an **exhibition** and unique networking opportunity for vendors of tools and services for hardware and embedded software for the design, development and test of Systems-on-Chip, IPs, Embedded Systems, ASICs, FPGAs and PCBs including a broad range of design reuse technologies and services.

To inform attendees on commercial and design related topics, there will be a full programme in the Exhibition Theatre which will combine presentations by exhibiting companies, best-practice reports by industry leaders on their latest design projects and selected conference special sessions.

TOPIC AREAS topic areas for submission

Within the scope of the conference, the main areas of interest are organised in the following tracks. Submissions can be made to any of the track topics. *For detailed descriptions of the track topics, please refer to the DATE website.*

Track D: Design Methods and Tools, addressing design automation and design tools for electronic and embedded systems. Emphasis is on methods and tools related to the use of computers in designing products. This includes designer feedback on existing design methods and tools as well as to initiate discussions on requirements of future system architectures, design flows and environments.

This track is organised in the following topics:

- D1 System Specifications, Models and Methodologies
- D2 System Design, Synthesis and Optimization
- D3 Simulation and Validation
- D4 Design for power, variability and ageing
- D5 Power Estimation and Optimisation
- D6 Emerging Technologies, Systems and Applications
- D7 Formal Methods and Verification
- D8 Network on Chip
- D9 Architectural and Microarchitectural Design
- D10 High-Level Synthesis
- D11 Reconfigurable Computing
- D12 Logic Synthesis and Timing Analysis
- D13 Physical Synthesis and Verification
- D14 Analog and Mixed-Signal Systems and Circuits
- D15 Parasitic Extraction, Modeling and Optimization of Interconnect, TSV and Power Grids

Track A: Application Design, is devoted to the presentation and discussion of design experiences with a high degree of industrial relevance, as well as innovative design methodologies and applications of specific design technologies. Contributions should illustrate state-of-the-art or record-breaking designs, which will provide viable solutions in tomorrow's silicon and embedded systems. In topic A7, there is the opportunity to submit short, 2-page papers that relate to industrial research and practice.

This track is organised in the following topics:

- A1 Green Computing Systems
- A2 Communication, Consumer and Multimedia Systems
- A3 Automotive Systems and Smart Energy Systems
- A4 Ambient Intelligence and Ultra-Low Power Systems for Healthcare and Wellness
- A5 Secure Systems
- A6 Reliable and Reconfigurable Systems
- A7 Industrial Experiences Brief Papers

Track T: Test and Reliability, covering all test, design-for-test, reliability and design-for-reliability issues, at system-, chip-, circuit-, and device-level. Including also diagnosis, failure mode analysis, debug and post-silicon validation challenges.

This track is organised in the following topics:

- T1 Defects, Faults, Variability and Reliability Analysis and Modelling
- T2 Test Generation, Simulation, Diagnosis and System Test
- T3 Test for Mixed-Signal, Analog, RF, MEMS-bioMEMS-MOEMS
- T4 Design-for-Test, Test Compression, Test Access
- T5 On-Line Test, Fault Tolerance and Reliable System Design

Track E: Embedded Systems Software is devoted to modelling, analysis, design and deployment of Embedded Software. Areas of interest include methods, tools, methodologies and development environments. Emphasis will also be on model-based design and verification, embedded software platforms, software compilation and integration, realtime systems, cyber-physical systems, networked and dependable systems.

This track is organised in the following topics:

- E1 Real-time, Networked, and Dependable Systems
- E2 Compilation and Code Generation for Embedded Software
- E3 Model-Based Design and Verification for Embedded Systems
- E4 Embedded Software Architectures
- E5 Cyber-Physical Systems

In addition to the above conference tracks, we welcome proposals for **Special Sessions, Tutorials and Friday Workshops**, and submissions for the Special Days on **Advancing Electronics beyond CMOS** and **System-Level Design**.

COMMITTEES committees

A full list of the executive and programme committee members is available on the DATE website.

SPONSORS sponsors

The event is sponsored by the European Design and Automation Association, the EDA Consortium, the IEEE Council on EDA, ECSI, ACM – SIGDA, and RAS. In cooperation with ACM – SIGBED, IEEE Solid-State Circuits Society (SSCS), IFIP and IET.

INFORMATION information

Event Secretariat

K.I.T. Group GmbH
Münzgassee 2,
01067 Dresden, Germany

Tel: + 49 351 4967 541
Fax: + 49 351 4956 116
info@kitdresden.de

General Chair

Gerhard P. Fettweis
TU Dresden, Germany

gerhard.fettweis@tu-dresden.de

Programme Chair

Luca Fanucci
University of Pisa, Italy

luca.fanucci@iet.unipi.it

To add your name to the mailing list go to the website:
www.date-conference.com.