

DATE 08 REGISTRATION FORM (All Fees in Euros) Please tick appropriate registration rates and write totals in right hand column			
	Payment received before 20 Feb 08	Payment Received after 20 Feb 08	Payment
CONFERENCE RATES			
Speakers and Members	C1 <input type="checkbox"/> 422	C2 <input type="checkbox"/> 546	
Delegates	C3 <input type="checkbox"/> 535	C4 <input type="checkbox"/> 690	
Students without DATE Party	C5 <input type="checkbox"/> 298	C6 <input type="checkbox"/> 390	
Students with DATE Party	C7 <input type="checkbox"/> 350	C8 <input type="checkbox"/> 452	
ONE DAY RATES			
One-Day Registration	D1 <input type="checkbox"/> 258	D2 <input type="checkbox"/> 334	
Select Day	Tue <input type="checkbox"/>	Wed <input type="checkbox"/>	Thu <input type="checkbox"/>
TUTORIAL RATES			
Speakers and Members	T1 <input type="checkbox"/> 210	T2 <input type="checkbox"/> 272	
Delegates	T3 <input type="checkbox"/> 268	T4 <input type="checkbox"/> 345	
Students	T5 <input type="checkbox"/> 195	T6 <input type="checkbox"/> 252	
TUTORIALS (MONDAY) Select a combination of one full-day tutorial (A, B, C) or two half-day tutorials, i.e. F1, G1, & F2, G2...			
Full Day Tutorials (Select One) A <input type="checkbox"/> Automatically Realising Embedded Systems... B <input type="checkbox"/> Design Flows, Communication Based Design... C <input type="checkbox"/> System-Level Design & Application Mapping...			
Morning Tutorials (select one) D1 <input type="checkbox"/> Design Variability: Challenges & Solutions... E1 <input type="checkbox"/> Power Gating for Ultra-low Leakage:... F1 <input type="checkbox"/> Heterogeneous System-Level Specification... G1 <input type="checkbox"/> Power-Aware Testing and Test Strategies...			
Afternoon Tutorials (select one) D2 <input type="checkbox"/> From Transistor to PLL – Analogue Design... E2 <input type="checkbox"/> DfM in the Analogue & Digital World F2 <input type="checkbox"/> Formal Methods in System & MPSoC... G2 <input type="checkbox"/> Soft Errors: System Effects, Protection...			
FRIDAY WORKSHOPS		before 20/2 X1 <input type="checkbox"/> 134	after 20/2 X2 <input type="checkbox"/> 170
Select One W1 <input type="checkbox"/> Reconfigurable Hardware... W2 <input type="checkbox"/> Impact of Process Variability on Design & Test W3 <input type="checkbox"/> Heterogeneous System Integration... W4 <input type="checkbox"/> The New Wave of the High-Level Synthesis W5 <input type="checkbox"/> Software Compilers... (SCOPES'08) W6 <input type="checkbox"/> Software Engineering for Embedded Systems W7 <input type="checkbox"/> Dependable Software Systems W8 <input type="checkbox"/> Modelling & Analysis of Real-Time...			
RECEPTIONS AND DATE PARTY S Extra ticket(s) @ €60			
Payment Method		TOTAL FEES DUE	
Cash <input type="checkbox"/> Cheque <input type="checkbox"/> Credit Card <input type="checkbox"/>			

PLEASE COMPLETE ALL SECTIONS (in block CAPITALS)

Forename.....Last Name.....

Job Title

Organisation.....

Address.....
.....
.....

Postcode.....Country.....

Telephone.....Fax.....

E-Mail.....

Member of Society.....Number.....

Tick if vegetarian meals required

PAYMENT BY CREDIT CARD

VISA MasterCard AMEX

Card Number.....

Expiry..... Card Verification No.:
(= last 3 digits on signature strip on reverse)

Cardholder's Address.....
.....
.....

Amount to be charged in Euros.....

Signature.....Date.....

You may register on <www.date-conference.com>
[VISA & MC only – excluding Students who must submit a confirmation of student status with their registration form]

Return this form by Fax or Mail before 20th February 2008 to qualify for the lower rate registration fees.

DATE Conference Secretariat (on behalf of EDAA)
European Conferences
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ATTENDEE SURVEY

1. Your Job Function (tick one only)

A Design / Engineering Management E Verification / Test
B General Management F EDA Development
C Chip Design / Architecture G Education
D Systems Design / Architecture H Marketing / Sales

2. Your Current Technical Design Concerns (multi -choice)

A Analogue / Mixed Signal I Manufacturing / Foundry
B ASIC / ASSP J MEMS
C DSP K PCB / MCM
D Embedded CPU / MCU L Real Time OS
E Embedded Software M RF / Microwave
F EMC / Thermal N Systems-on-Chip
G FPGA / PLD O Test / Validation
H IP Cores P Verification