Get ready for the 52nd DAC to be held in San Francisco, June 7 - 11, 2015. Continuing the tradition of being in the forefront of electronic design, the 52nd DAC will offer outstanding training and education as well as superb networking opportunities. Check out www.dac.com.

Abstract Deadline - Friday, November 21, 2014

ASP-DAC 2015 is the twentieth annual international conference on VLSI design automation in Asia and South Pacific region, one of the most active regions of design and fabrication of silicon chips in the world. The conference aims at providing the Asian and South Pacific CAD/DA and Design community with opportunities of presenting recent advances and with forums for future directions in technologies related to Electronic Design Automation (EDA). The format of the meeting intends to cultivate and promote an instructive and productive interchange of ideas among EDA researchers/developers and system/circuit/device designers. All scientists, engineers, and students who are interested in theoretical and practical aspects of VLSI design and design automation are welcomed to ASP-DAC.

General Information

- Date: Jan. 19-22, 2015
- Place: Makuhari, Japan
- General Chair: Kunio Uchiyama (Hitachi)
- Technical Program Chair: Naehyuck Chang (Seoul National Univ.)
- Design Contest Co-Chairs: Hiroyuki Ito (Tokyo Institute of Technology)
  - Noriyuki Miura (Kobe University)
- Submission of Papers / Design Descriptions:
  - Deadline for submission:
    - regular paper: 5 PM JST (UTC+9) July 11 (Fri), 2014
    - UDC: 5PM JST (UTC+9) July 11 (Fri.), 2014
  - Notification of acceptance: Sep. 15 (Mon), 2014
  - Deadline for final version: 5 PM JST (UTC+9) Nov. 10 (Mon), 2014

The Japan Electronics and Information Technology Industries Association (JEITA: Ryoji Chubachi, Chairman / Representative Corporate Executive Officer and Vice Chairman of Sony Corporation) will hold the Electronic Design and Solution Fair (EDS Fair) in Yokohama International Peace Convention Center (Pacifico Yokohama) over three days from November 20 (Wednesday) to 22 (Friday), 2013. EDS Fair is a professional exhibition of the electronic design automation (EDA) industry specializing in design solutions, design technologies and EDA technologies, as well as IP solutions and design services that utilize these. This will be the 14th time this event has been held.

ICCAD is the most well-established CAD conference in the industry. ICCAD has a long standing tradition of producing a cutting-edge, innovative technical program for attendees. 2014 will be no different! November 3-6, 2014, Hilton San Jose, San Jose, CA, USA.

ICCAD serves EDA and design professionals, highlighting new challenges and innovative solutions for Integrated Circuit Design Technologies and Systems. ICCAD covers the full range of CAD topics – from
device and circuit-level CAD up through system-level CAD and embedded software, as well as CAD for post-CMOS design and novel application areas, such as biology and nanotechnology.