DATE 2016 in Dresden: Highlighting Automotive and Secure Systems

Call for Papers

DATE is the major international event for design and engineering of System-on-Chip, System-on-Board and Embedded Systems Software and states a unique networking opportunity, bringing together designers and design automation users, vendors and vendors, as well as specialists in hardware and software design, test and manufacturing of electronic circuits and systems.

After the successful editions in Dresden in 2010, 2012, and 2014, DATE 2016 will take place again from 14 to 18 March, 2016, at the International Congress Center Dresden, Germany. It is located at the banks of the medieval Elbe at walking distance from the city centre and provides state-of-the-art conference facilities.

Dresden is a modern and vibrant city with short distances and a cutting-edge technology and economy sector. It is a leading semiconductor site in Europe, concentrating expertise on research, development and manufacturing of leading-edge technologies that drive semiconductor industry to the top. Major players are e.g. GLOBALFOUNDRIES, Infineon, and the Technical University, which is part of the German Excellence Initiative. Silicon Saxony is a registered industry association of 300 companies in the microelectronics and related sectors in Saxony, Germany, with around 20,000 employees. Moreover, Dresden has 11 Fraunhofer institutes, including the Fraunhofer Center for Nanoelectronic Technologies (CENIT), the IPK, the HZDR and further research institutes devoted to semiconductors. It therefore states an excellent venue for DATE 2016.

The five-day event addresses all aspects of research into technologies for electronic and embedded system engineering. The Call for Papers shows the wide scope of technical contributions, covering the design process, test, and automation tools for electronics ranging from integrated circuits to distributed embedded systems. This includes both hardware and embedded software design issues. The conference scope also includes the elaboration of design requirements and new architectures for challenging application fields such as telecoms, wireless communications, multimedia, healthcare and assistive technology, smart energy, automotive and secure systems.

All papers have to be submitted electronically by September 13, 2015, for standard oral presentation or for interactive presentation.

Two Special Days

In the programme will focus on areas bringing new challenges to the system design community: Automotive Systems and Secure Systems. Each of the Special Days will feature a full programme of keynote speeches, tutorials and technical presentations.

High-end cars today have more than 100 ECUs connected by various communication buses, that are used to run several million lines of software code spanning across safety critical, driver assistance and comfort related application domains. Designing, verifying and upgrading such a complex hardware/software system poses several challenges for EDA engineers, computer architects, and embedded systems and software designers. Hence, there are many interesting opportunities for the EDA and the embedded systems community in the automotive domain that the Automotive Systems special day at DATE 2016 will seek to explore in the form of various technical sessions and discussion panels.

Security is essential to create a safe and trustworthy Information and Communications Technology infrastructure. Electronics are becoming more and more integrated into the environment: the Internet of Things, Cyber-Physical Systems, and Smart Cities are all novel phenomena. Data is stored in and travels from implanted medical devices to the cloud over wireless links and through optical fibers. None of this revolution will be sustainable without adding security. During the Secure Systems special day, several aspects of making systems secure will be addressed.

The conference is complemented by an exhibition, running for three days (Tuesday – Thursday). It is a unique networking opportunity for vendors of tools and services for hardware and embedded software for the design, development and test of System-on-Chip, IPs, Embedded Systems, ASSIC, FPGAs and PCBs including a broad range of design reuse technologies and services. Furthermore, it states the perfect venue for industries to meet University Professors to foster their University Programme and especially for PhD Students to meet their future employers. There will be a full programme at the Exhibition Theatre again, combining presentations from exhibiting, best-practice reports by industry leaders on their latest design projects and selected conference special sessions. This year special attention will be given to proposals matching exhibition thematic campuses on 5G wireless network, 3D-IC integration, FD-SOI, IoT, Automotive and Secure systems.

Submissions are invited from industry consortia, technology groups, standard bodies, or any person with opinions on future industry trends by October 18, 2015.

On the first day of the DATE week, in-depth technical tutorials are given by leading experts in their respective fields. The tutorials are well suited for researchers, tool developers and system designers. On Friday, workshops concentrate on emerging research and application topics. Proposals for Monday Tutorials and Friday Workshops have to be submitted electronically by September 13, 2015.

The whole DATE 2016 Executive Committee also encourages proposals for Special Sessions, European Projects, the University Booth as well as the PhD Forum.

For further information and the detailed Call for Papers please visit:
www.date-conference.com

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The 19th DATE conference and exhibition is the main European event bringing together designers and design automation users, researchers and vendors, as well as specialists in the hardware and software design, test and manufacturing of electronic circuits and systems. It puts strong emphasis on ICs/SoCs, reconfigurable hardware and embedded systems, including embedded software.

The five-day event consists of a conference with plenary invited papers, regular papers, panels, hot-topic sessions, tutorials and workshops, two special focus days and a track for executives. The scientific conference is complemented by a commercial exhibition showing the state-of-the-art in design and test tools, methodologies, IP and design services, reconfigurable and other hardware platforms, embedded software, and (industrial) design experiences from different application domains, e.g., automotive, wireless, telecom and multimedia applications. The organisation of user group meetings, fringe meetings, a university booth, a PhD Forum, vendor presentations and social events offers a wide variety of extra opportunities to meet and exchange information on relevant issues for the design and test community. Special space will also be allocated for EU-funded projects to show their results.

The Conference

The conference addresses all aspects of research into technologies for electronic and (embedded) systems engineering. It covers the design process, test, and tools for design automation of electronic products ranging from integrated circuits to distributed large-scale systems. This includes both hardware and embedded software design issues. The conference scope also includes the elaboration of design requirements and new architectures for challenging application fields such as telecommunication, multimedia, healthcare and automotive systems. Persons involved in innovative industrial designs are particularly encouraged to submit papers to foster the feedback from design to research. Panels, hot-topic sessions and embedded tutorials highlight and inform about emerging topics.

Special Days

The programme will focus on ten areas bringing new challenges to the system design community.

Automotive Systems: High-end cars today have more than 100 ECUs connected by various communication buses like CAN, FlexRay and Ethernet, that are used to run several millions of lines of software code spanning across safety-critical, driver assistance and comfort related application domains. Designing, verifying and upgrading such a complex hardware/software system poses a variety of challenges for EDA engineers, computer architects, and embedded systems software designers. Further, most of the innovation in the automotive domain is now in electronics and software, as is seen in the form of various new driver assistance systems and functionalities. Hence, there are many interesting opportunities for the EDA and the embedded systems community in the automotive domain, with the automotive special day at DATE 2015 will seek to explore in the form of various technical sessions and discussion panels.

Secure Systems: Security is essential to create a safe and trustworthy Information and Communications Technology infrastructure. Electronics are becoming more and more integrated into the environment: the Internet of Things, Cyber-Physical Systems, Smart Cities are all novel phenomena. Data is stored in and travels from implanted medical devices to the cloud over wireless links and through optical fibers. None of this revolution will be sustainable without adding security. During this special day, several aspects of making systems secure will be addressed. We plan to look at secure HW and SW platforms, address different application domains: automotive, smart grid, or smart factory. We address security versus other functional requirements as dependability and testability, and we investigate design practices for security.

On the first day of the DATE event, half-day in-depth technical tutorials are given by leading experts in their respective fields. The tutorials are well suited for researchers, tool developers and system designers.

Friday Workshops

Friday Workshops concentrate on special areas and novel topics.

The Exhibition

The conference is accompanied by a comprehensive exhibition, which states a unique networking opportunity for vendors of tools and services for hardware and embedded software for the design, development and test of System-on-Chip, IPs, Embedded Systems, ASICs, FPGAs and PCBs including a broad range of design reuse technologies and services.

To inform attendees on commercial and design related topics, there will be a full programme in the Exhibition Theatre which will combine presentations by exhibiting companies, best practice reports by industry leaders on their latest design projects and selected conference special sessions.

Topic Areas for Submission

Within the scope of the conference, the main areas of interest are organized in the following tracks. Submissions can be made to any of the track topics.

Track D: Design, Methods and Tools, addressing design automation, design tools and hardware architectures for electronic and embedded systems. Emphasis is on methods, algorithms and tools related to the use of computer in designing complex systems. This includes significant improvements on existing design methods and tools as well as forward-looking approaches to model and design future system architectures, design flows and environments.

This track is organized in the following topics:

- D1 System Specification and Modeling. Click here for details (31)
- D2 System Design, High-Level Synthesis and Optimization. Click here for details (22)
- D3 System Simulation and Validation. Click here for details (23)
- D4 Formal Methods and System Verification. Click here for details (24)
- D5 Design and Test for Analog and Mixed-Signal Systems and Circuits. Click here for details (30)
- D6 Emerging Technologies and Systems. Click here for details (31)
- D7 Power Modeling, Optimization and Low-Power Design. Click here for details (27)
- D8 Network on Chip. Click here for details (32)
- D9 Architectural and Microarchitectural Design. Click here for details (32)
- D10 Temperature and Variability Aware Design and Optimization. Click here for details (33)
- D11 Reconfigurable Computing. Click here for details (31)
- D12 Logical and Physical Analysis and Design. Click here for details (28)

Track A: Application Design, is devoted to the presentation and discussion of design experiences with a high degree of industrial relevance, as well as innovative design and test methodologies, and applications of specific design and test technologies. Contributions should illustrate state-of-the-art or record-breaking designs, which will provide viable solutions in tomorrow's silicon and embedded systems. In topic A7, there is the opportunity to submit short, 2-page papers that relate to industrial research and practice.
This track is organised in the following topics:
- T1 Defects, Faults, Variability and Reliability Analysis and Modeling
- T2 Test Generation, Simulation and Diagnosis
- T3 Design-for-Test, Test Compression and Access
- T4 On-Line Test, Fault Tolerance and Robust Systems
- T5 Design and Test for Analog and Mixed-Signal Circuits and Systems

Track T: Test and Robustness, covering all test, design-for-test, reliability and design-for-robustness issues, at system-, chip-, circuit-, and device-level for both analog and digital electronics. Including also diagnosis, failure mode analysis, debug and post-silicon validation challenges.

This track is organised in the following topics:
- E1 Real-time, Networked, and Dependable Systems
- E2 Compilers and Software Synthesis for Embedded Systems
- E3 Model-based Design and Verification for Embedded Systems
- E4 Embedded Software Architectures
- E5 Cyber-Physical Systems

Track E: Embedded Systems Software, is devoted to modelling, analysis, design and deployment of embedded software. Areas of interest include methods, tools, methodologies and development environments. Emphasis will also be on model-based design and verification, embedded software platforms, software compilation and integration, real-time systems, cyber-physical systems, networked and dependable systems.

In addition to the above conference tracks, we welcome proposals for Special Sessions, Tutorials, and Friday Workshops, and submissions for the Special Days on Automotive Systems and Secure Systems.

Committees
Click the links below for a full list of the executive and programme committee members:
- DATE Executive Committee
- Technical Programme Committee

Sponsors
The event is sponsored by the European Design and Automation Association, the EDA Consortium, the IEEE Council on EDA, ECSI, ACM – SIGDA, and RAS.

In cooperation with IEEE Computer Society test technology technical Council (tttC), IEEE Solid-State Circuits Society (SSCS), International Federation for Information Processing (IFIP).

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To add your name to the mailing list go to the website: http://www.date-conference.com/user/register

Attachment
Size
DATE 2016 Call for Papers (PDF) 423.98 KB

Conference Highlights
Computingnow: Design Automation Test Europe 2015
2014-12-23
DATE 2014 in Grenoble: highlighting Electronics Design for the Internet of Things and for Medical Applications

2014-01-27

DATE is a leading international event and unique networking opportunity for design and engineering of System-on-Chip, System-on-Board and Embedded Systems Software. Since the selection of venues Dresden (2010-2012-2014) and Grenoble (2011-2013) has been a great success, the conference will again take place in Grenoble for DATE 2015.

The Alpexpo Congress Centre will host the five day event from 9 – 13 March, 2015. It is located in close proximity to central Grenoble and provides a modern, well equipped location for DATE.

The city of Grenoble, which is surrounded by outstanding natural beauty of the Alps, has a strong economy linked to the research and innovation activities performed by leading national and international large companies such as STMicroelectronics, IBM, Schneider Electric, France Telecom, Thales, Hewlett-Packard, Sony, Xerox and others with more than 38,000 people in micro and nano technologies (24,000) and 14000 in embedded software. It is the second largest research centre in France with 15 research centres and universities (CEA-leti – 1,700 researchers, TUMA, Vietnam) and more than 100 startup companies located in the area in addition to 117 companies – 81% of which are SMEs.

DATE is working closely with local industry to increase the visibility of the event and to maximize quality visitor attendance.

The DATE conference addresses all aspects of research into technologies for electronic and embedded system engineering. It covers the design process, test, and automation tools for electronics ranging from integrated circuits to distributed embedded systems. This includes both hardware and embedded software design issues. The conference scope also includes the elaboration of design requirements and new architectures for challenging application fields such as telecomm, wireless communications, multimedia, healthcare, smart energy and automotive systems. The whole DATE 2015 Executive Committee highly welcomes all submissions for standard oral presentation or for interactive presentation. All papers have to be submitted electronically by September 14, 2014.

Two Special Days in the programme will focus on areas bringing new challenges to the system design community. Each of the Special Days will have a full programme of keynote, panels, tutorials and technical presentations.

Designing Electronics for the Internet of Things: Our society is evolving to a point where objects and people will be almost permanently connected and exchanging information. This scenario, called the Internet of Things (IoT), is the result of the convergence in the evolution and integration of communication, computing, storage and sensing technologies. While the potential impact of IoT in our daily life is enormous, there are major challenges related to microelectronics design, novel paradigms for data acquisition and fusion, as well as analysis and storage of the large volume of collected information. This special day investigates how the progress in technologies and applications of IoT can be nurtured and cultivated for the benefit of society.

Designing Electronics for Medical Applications: Progress in microelectronics has enabled the miniaturization of data processing elements, radio transceivers and sensors for a large set of physiological phenomena. Autonomous sensor nodes can monitor vital body parameters in an unobtrusive way during daily life. However, the inherent resource-constrained nature of these systems, coupled with the specific operating conditions, the stringent autonomy requirements, and the need for automated analysis for complex biological signals pose important design challenges. This special day will cover the latest trends towards alternative architectures, technologies and design paradigms for low cost, low power, miniature devices such as smart wireless sensor nodes.

The conference is complemented by an exhibition, running for three days (Tuesday – Thursday), which states a unique networking opportunity for vendors of tools and services for hardware and embedded software for the design, development and test of System-on-Chip, IP cores, Embedded Systems, ASICs, FPGAs and PCBs including a broad range of design reuse technologies and services.

To inform attendees on commercial and design related topics, there will be a full programme in the Exhibition Theatre which will combine presentations by exhibiting companies, best practice reports by industry leaders on their latest design projects and selected conference special sessions. Submissions are invited from industry consortia, technology groups, standards bodies, or any person with opinions on future industry trends by October 19, 2014.

On the first day of the DATE event, in-depth technical tutorials are given by leading experts in their respective fields. The tutorials are well suited for researchers, tool developers and system designers. Friday Workshops concentrate on specialized emerging research and application topics. Proposals for Tutorials and Friday Workshops have to be submitted electronically by September 14, 2014.

DATE 2014: Advance Programme available online

2014-01-27

The DATE 2014: Advance Programme is available online at [http://www.date-conference.com/conference/event-overview](http://www.date-conference.com/conference/event-overview)

We proudly present to you the Advance Programme of DATE 14. DATE combines the world’s favorite electronic systems design and test conference with an international exhibition for electronic design, automation and test, from system-level hardware and software implementation right down to integrated circuit design.

DATE 14 received 1090 paper submissions, an all-time high for DATE and an 8% increase over DATE 13. 890 of those submissions were eligible for review. DATE combines the world’s favorite electronic systems design and test conference with an international exhibition for electronic design, automation and test, from system-level hardware and software implementation right down to integrated circuit design.

DATE 14 received 1090 paper submissions, an all-time high for DATE and an 8% increase over DATE 13. 890 of those submissions were eligible for review. DATE is truly an international conference. Besides the large share (45%) of submissions coming from Europe, 23% of submissions are from North America, 27% from Asia, and 4% from the rest of the world. This clearly demonstrates DATE’s global reach and impact.

The most attractive topics this year were: "Architectural and Microarchitectural Design" in the Design Methods and Tools Track, "Secure Systems" in the Application Design Track, "On-Line Test, Fault Tolerance and Reliable System Design" in the Test and Reliability Track and "Real-time, Networked, and Dependable Systems" in the Embedded Systems Software Track.

For the 17th successive year DATE has prepared an exciting technical program, with the help of more than 300 members of the Technical Program Committee who dedicated their time to perform 3620 reviews (more than four per submission), and select 206 (=23.1%) papers for regular presentation and 107 (= 12.0%) for interactive presentation.

This year the conference will be held in Germany, at the international Congress-Center (ICC) in Dresden and will span an entire week from Monday March 24 with tutorials, and ending on Friday March 28 with workshops.

The plenary keynote speakers on Tuesday are David Fuller, Vice President of Application and Embedded Software for National Instruments, to talk about "System Design Challenges for Next Generation Wireless and Embedded Systems" and Gerd Teepe, Design Director Engineering at GLOBALFOUNDRIES in Dresden, to talk about "The Growing Importance of Microelectronics from a Foundry Perspective." On the same day, the Executive Track offers a series of business
panels discussing hot topics. Executive speakers from Synopsys, Cadence, IBM, IMEC, TSMC Europe, Mentor Graphics and many other companies leading the design and automation industry will address some of the complexity issues in electronics design and discuss about the advanced technology challenges and opportunities. To emphasize that DATE is the major event for designers, DATE 14 features invited sessions where Europe's famous consumer industry presents its best designs and design practices.

The main conference program from Tuesday to Thursday includes 77 technical sessions organized in parallel tracks from four areas:

D = Design Methods and Tools
A = Application Design
T = Test and Reliability
E = Embedded Systems Software

Extra tracks are dedicated to the Executive Day on Tuesday and the ten special days: "System-Level Design" Day on Wednesday and "Advancing Electronics Beyond CMOS" Day on Thursday.

There is a lunch-time keynote on Wednesday by Michael Bolle (Bosch, DE), who will talk on "Auto cockpit of the future". A second lunch-time keynote on Thursday by Karl Leo (Fraunhofer Institute, DE) will be on "Organic Electronics – From Lab to Market". Additionally, there are 107 Interactive Presentations which are organized into five IP sessions.

Finally, DATE offers a comprehensive overview of commercial verification tools in its exhibition including vendor seminars and abundant networking possibilities with fringe meetings.

We wish you a productive and exciting DATE 14 and a memorable social party on Wednesday evening.

DATE 14 General Chair
Gerhard Fettweis
TU Dresden, DE

DATE 14 Program Chair
Luca Fanucci
University of Pisa, IT

Record number of submissions for DATE 2014, highlighting Test, Security, Electronics Beyond CMOS and System-Level Design [65]

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For the 17th successive year DATE has prepared an exciting technical program. With the help of 339 members of the Technical Program Committee who carried out 3620 reviews (more than four per submission), finally 206 (=23.1%) papers were selected for regular presentation and 107 (>12.0%) for interactive presentation.

In addition, the DATE conference programme includes several hot-topics sessions and embedded tutorials, highlighting special interest topics in the areas of Automotive, Security, Reliability, Mixed-Signal and Emerging Transistor Technologies, as well as several panels covering EDA challenges in emerging technologies and debating future directions. Twelve half-day tutorials will be given on Monday, with topics ranging from Energy-Efficient System Design to Post-Silicon Validation and Debug. On Friday several full-day workshops cover a large number of hot topics like dependable GPU computing, simulation for cyber-physical systems, multicore architectures at nanoscale or 3D Integration.

The conference online registration and the complete advance programme will be available on the DATE website starting from mid-December 2013.

Special Day on System Level Design

“The exploitation of recent technology achievements such as more and more processor cores and other IP modules available on a chip today is currently of big interest to many developers in application areas of embedded systems such as automotive, industrial automation and aviation. But there are also very challenging expectations on the achievable improvements and on the available support by tool vendors and system houses to aid handling this increasing system complexity”, says Jürgen Teich, co-chair of the DATE 2014 Special Day on System Level Design.

This special day will therefore reflect current industrial practices as well as present recent advances in the System Level Design research area. A particular emphasis will be on ultra-low power design and modelling at multiple abstraction levels, virtual platforms for software development and architecture design of MPSoCs. A panel on "TANDEM Co-Development – The Industrial Workflow" will take place in the afternoon to discuss opportunities and challenges of joint hardware and software development. Moreover, two hot-topics sessions in the morning on "The fight against Dark Silicon" and "Predictable Multi-Core Computing" and a special session on "System Simulation and Virtual Prototyping" in the afternoon will comprise this special day's schedule.

A particular highlight of the day is the special day's keynote speech by Dr. Michael Bolle (Executive Vice President Engineering, Bosch), talking about the "Auto cockpit of the future".

Special Day on Advancing Electronics Beyond CMOS

As the issues associated with CMOS scaling become harder and more costly to solve, this special day will cover the latest trends towards alternative devices and paradigms for computing and data acquisition, storage and transport. In particular, this special day begins with two hot-topics sessions on "CMOS scaling: evolutionary to revolutionary" and "Memories today and tomorrow". An embedded tutorial on "Alternatives to CMOS" takes place after lunch, followed by a last hot-topics session on "The future of interfacing to the natural world".

The special day's keynote speaker, Prof. Karl Leo (Director, Fraunhofer Institute COMEDD), will talk about "Organic Electronics – From Lab to Markets".

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General Information Registration & Participation (i)

2014-11-06

The online registration for DATE 2015 is closed now.

The registration to the conference will still be possible on-site at the registration desk (Alpexpo Congress Centre, entrance foyer).

In case of any questions, please do not hesitate to contact us via email: conference-management@date-conference.com.

Kindly note:

- Speakers are kindly asked to provide the SoftID(s) of the accepted paper(s). Each accepted paper requires one full conference registration at the speaker rate (i.e. of the main author or any of its co-authors).
- IEEE/ACM members are kindly asked to provide their member number when registering online.
- Students are kindly asked to send a full proof of student status to the Event Secretariat by email: conference-management@date-conference.com or fax: +49 351 4956 116, i.e. a scanned copy of the student ID card and a letter from a Professor or Head of Department to confirm the student status at the time of conference.
- Press representatives can participate for free at the conference including access to all sessions, the exhibition and social events. A press identification card must be provided upon registration.
- The use of the exhibition is for free. Please choose the participant type "Exhibition Visitor".

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<td></td>
</tr>
<tr>
<td>TUTORIAL fee (Monday)</td>
<td></td>
<td></td>
</tr>
<tr>
<td>Full-Day (Speakers &amp; IEEE/ACM Members)</td>
<td>200.00 €</td>
<td>250.00 €</td>
</tr>
<tr>
<td>Full-Day (Delegates)</td>
<td>225.00 €</td>
<td>275.00 €</td>
</tr>
<tr>
<td>Full-Day (Students)</td>
<td>150.00 €</td>
<td>200.00 €</td>
</tr>
<tr>
<td>Half-Day (pm)</td>
<td>125.00 €</td>
<td>175.00 €</td>
</tr>
<tr>
<td>WORKSHOP fee (Friday)</td>
<td></td>
<td></td>
</tr>
<tr>
<td>Full-Day workshop</td>
<td>200.00 €</td>
<td>250.00 €</td>
</tr>
<tr>
<td>Exhibition Reception &amp; DATE Party (Tuesday &amp; Wednesday)</td>
<td></td>
<td></td>
</tr>
<tr>
<td>Extra ticket (each)</td>
<td>60.00 €</td>
<td></td>
</tr>
</tbody>
</table>

The CONFERENCE registration includes:

- Access to all sessions and to the exhibition area
- Conference bag and conference proceedings (available for download online)
- Entrance to the Exhibition Reception (Evening, Tuesday, March 10, 2015)
- Entrance to the DATE PARTY (Evening, Wednesday, March 11, 2015), except for the student rate
- Refreshments during coffee breaks, snacks during lunch break

The DAY TICKET registration includes:

- Access to all sessions at the day indicated
- Access to the exhibition area
- Conference bag and conference proceedings (available for download online)
- Refreshments during coffee breaks, snacks during lunch break at the day indicated
- Entrance to the Exhibition Reception (Evening, Tuesday, March 10, 2015)
- NO entrance to the DATE Party

The EXHIBITION registration includes:

- Access to the exhibition area
- Event Guide
- Entrance to the Exhibition Reception (Evening, Tuesday, March 10, 2015)
- NO entrance to the DATE Party

The TUTORIAL registration includes:

- Access to the chosen tutorial (please choose only one a.m. tutorial and/or one p.m. tutorial as they take place in parallel)
- Documentation (available for download online)
- Refreshments during coffee breaks, only for full-day tutorial: snacks during lunch break

The WORKSHOP registration includes:

- Access to the chosen workshop (please choose only one workshop as they take place in parallel)
- Documentation (available for download online)
- Refreshments during coffee breaks, snacks during lunch break

Payment methods

Payment can be made by credit card or by bank transfer.
Payment by credit card can be done during the online registration process. No credit card fee will be charged. You will get a confirmation of registration and payment afterwards via e-mail. For paying by bank transfer, you will receive an invoice including all payment details via e-mail. All bank charges have to be covered by the delegate.

The registration for the conference is only valid after receipt of the full registration fees. If you have not paid the fees within the mentioned deadline (Tuesday, February 10, 2015), the higher fee will apply and you will receive a new invoice from the Event Secretariat subsequently.

**Hotel reservation**

Accommodation in Grenoble can be directly booked by means of the online reservation platform of the service provider HRS:

**DATE 2015 Hotel Reservation**

For more information, please visit the homepage "Venue & Hotels".

**VISA Letter Application**

A VISA letter can be requested during the online registration process (full payment of the registration fees is required). VISA Letters will only be sent to fully registered conference delegates, not to Exhibition Visitors.

**General Terms and Conditions**

**Registration**

The registration to the DATE 2015 conference is widely possible via the online registration form. Everyone who wants to attend must register online. With the receipt of the online registration the contract between K.I.T. Group and the participant is legally binding. The Event Secretariat K.I.T. Group is authorized to act on behalf of the conference organizer EDAA (edm.sci.cio imec. Kapeldreef 75, B-3001 Leuven, Belgium).

The participant will receive an official confirmation of registration/issue from K.I.T. Group via email. The conference fees and the cost of other booked items must be paid in due time as stated on the invoice. For applying for the early registration fee, payment in full must be made until Tuesday, February 10, 2015. Otherwise, the Event Secretariat will charge the registration to the late registration fee and send a new invoice. All conference fees must be fully paid prior to the conference.

Payment can be made by credit card or by bank transfer. Any bank charges have to be paid by the participant. If the conference fees have not been paid until the beginning of the conference, they need to be paid on-site in cash or by credit card. Cheques cannot be accepted.

**Speaker Registration**

Speakers of accepted papers will not receive any refund in case of cancellation due to the publication policy of accepted papers in the DATE proceedings (only those papers which are linked to at least one fully paid conference registration at the speaker rate will be published in the proceedings).

**Cancellation of registration**

In case of cancellation of the registration or cancellation of single bookings received until Friday, January 30, 2015, a cancellation fee of 50% of the registration fee applies. No refund will be given for cancellations made after Friday, January 30, 2015, and the claim for the full payment of all registration fees remains, respectively. In case of cancellation or non-dedicated usage of related services, all conference fees minus a processing fee of 50.30 € will be refunded after having received a proof of the rejection from the corresponding embassy via email, mail or fax. All registration changes and cancellations must be communicated to K.I.T. Group in written form, i.e. via email, mail or fax.

Workshops: If the attendance of one of the Friday workshops is below 20, the organizer reserves the right to cancel the workshop. In this case, participants will be informed in due time and receive a full refund of the paid fees.

**Insurance**

The registration fee does not include insurance of any kind. The organizers cannot accept any responsibility or liability for injuries or losses sustained as a result of any cancellations, accidents, illness or other occurrences that may arise in connection with the conference or other events. Participants are advised to organize appropriate travel and health insurance themselves.

**Liability**

K.I.T. only acts as mediator and is not liable for any losses of things, in accidents, destructions and damages of property and in the injury of persons, the reason does not matter. Participants and accompanying persons participate in all conference events on their own risk.

**Place of fulfillment**

The court of jurisdiction is Leuven. The law of the Kingdom Belgium applies.

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**Extended Submission Deadline for DATE 2014 Monday Tutorials**

2013-09-12

Proposals should be submitted before Sunday, September 15, 2013, 23:59:59 CEST.

By this deadline at least the tutorial title and the organizer need to be registered in the online submission system. The second ("extended") deadline is Friday, September 18, 2013, 23:59:59 CEST. By this second deadline, a full submission including the complete description of the tutorial needs to be uploaded in the online submission system.

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**Scientific and Industrial Experiences Brief Papers Submission Deadlines**

2013-09-12

- Sunday, September 15, 2013, 23:59:59 CEST
- Friday, September 18, 2013, 23:59:59 CEST

Read the Submission Instructions for details.

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**Advancing Electronics Beyond CMOS and System Level Design at DATE 2014 in Dresden**

2013-09-04

**Call for Papers**

DATE is a leading international event and unique networking opportunity for design and engineering of System-on-Chip, System-on-Board and Embedded Systems Software. Since the selection of venues Dresden (2010-2012) and Grenoble (2011-2013) has been a great success, the conference comes back to Dresden for DATE 2014.

DATE 2014 will take place at the International Congress Center Dresden, Germany, from 24-28 March 2014. Dresden, located in Germany, and is a leading key semiconductor site in Europe. It concentrates expertise on research, development and manufacturing of leading-edge technologies in order to drive semiconductor industry at the top. Major players are e.g. the Technical University which has recently succeeded in the German Excellence Initiative, GLOBALFOUNDRIES and Infineon. The cooperation among all these players has evolved Silicon Saxony, a collaborative association with around 200 companies (40,000 employees) that represents the largest center of microelectronics in Europe. Dresden, the "City of Science", has 11 Fraunhofer institutes, including Fraunhofer Center for Nondestructive Technologies (CNT), the IFW, the HZDR and further research institutes devoted to semiconductors.

The conference addresses all aspects of research into technologies for electronic and embedded system engineering. The Call for Papers shows the wide scope of technical contributions, covering the design process, test, and tools for design automation of electronic products ranging from integrated circuits to distributed large-scale systems. This includes both hardware and embedded software design issues. The conference scope also includes the elaboration of design requirements and new architectures for challenging application fields such as telecommunication, sensor networks, displays, MEMS.

The special day System-Level Design will reflect current industrial practices as well as present recent advances in this research area. A particular emphasis will be on ultra-low power design and modeling at multiple abstraction levels, virtual platforms for software development and architecture design of MPSoCs. We will also take a look at high-level synthesis from different input languages as well as software code generation techniques. Important topics also include...
multicore enablement for safety-critical and real-time embedded systems as well as accelerator-rich design for the fight against dark silicon.

The conference is complemented by an exhibition running for three days (Tuesday – Thursday). This represents a unique networking opportunity for vendors of tools and services for hardware and embedded software for the design, development and test of System-on-Chip, IPs, Embedded Systems, ASICS, FPGAs and PCBs including a broad range of design reuse technologies and services.

The Exhibition Theatre is an open discussion forum on the exhibition floor, which offers a mix of panel discussions and user testimonials that provide a dynamic overview of key technical and business issues in electronic and embedded systems design. Submissions are invited from industry consortia, technology groups, standards bodies, or any person with opinions on future industry trends by Sunday, October 18, 2015.

On the first day of the DATE event, in-depth technical tutorials are given by leading experts in their respective fields. The tutorials are well suited for researchers, tool developers and system designers. Friday Workshops concentrate on specialized emerging research and application topics. Tutorials and Friday Workshops Proposals have to be submitted electronically by Sunday, September 13, 2015 23:59:59 CET.[9]

DATE 2014 Press Contacts

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