FM01 EDM/EDAA/ACM SIGDA PhD Forum

Session Type: Fringe Meeting
Date: Monday, March 9, 2015
Time: 19:00-21:00
Location / Room: Salle de Reception

Organiser
Rolf Drechsler, University of Bremen/DFKI GmbH, DE (Contact Rolf Drechsler)

PhD Forum Committee Chair
Rolf Drechsler, University of Bremen/DFKI GmbH, DE (Contact Rolf Drechsler)

PhD Forum Committee Members
Walter Anheier, University of Bremen, DE
Laleh Behjat, University of Calgary, CA
Davide Bertozzi, Università degli studi di Ferrara, IT
Anupam Chattopadhyay, Nanyang Technological University, SG
Giorgio Di Natale, LIRMM, FR
Helmut Graeb, TU München, DE
Shiyan Hu, Michigan Technological University, US
Younghyun Kim, Purdue University, US
Xin Li, Carnegie Mellon University, US
Gi-Joon Nam, IBM, US
Sander Stuijk, Eindhoven University of Technology, NL
Daniel Tille, Infineon Technologies, DE
Miroslav N. Velev, Aries Design Automation, US
Natarajan Viswanathan, IBM, US
Robert Wille, University of Bremen/DFKI GmbH, DE

The ACM SIGDA/EDAA PhD Forum is part of the DATE Conference and hosted by ACM SIGDA and the European Design Automation Association (EDAA). It offers the opportunity for PhD students to present their thesis work to a broad audience in the design, automation and test community from academia and industry. During the presentation at the DATE Conference, it helps students to establish contacts. Also, representatives from industry and academia get a glimpse of state-of-the-art research in design, automation and test. The review process resulted in the selection of the PhD students listed below. We thank ACM SIGDA, EDAA, and DATE for making this Forum possible.

Agenda

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| 19:00  | FM01.1.1| Selective Transistor-Redundancy Based Fault Tolerance Technique for Combinational Circuits  
Speaker: Ahmad Sheikh, King Fahd University of Petroleum & Minerals, SA |
| 19:00  | FM01.1.2| Definition of Methods and Tools for an Effective Smart Buildings Deployment  
Speaker: Alessandro Antonio Nacci, Politecnico di Milano, IT |
| 19:00  | FM01.1.3| Design Methods for Reliable Quantum Circuits  
Speaker: Alexandru Paler, University of Passau, DE |
| 19:00  | FM01.1.4| Energy Efficient Cache Memories in Deeply-Scaled Technologies  
Speaker: Alireza Shafaei Bejestani, University of Southern California, US |
| 19:00  | FM01.1.5| Hybrid Wire and Surface-wave Communication Fabrics for Future NoC-based Chip Multiprocessors  
Speaker: Ammar Karkar, School of Electrical and Electronic Engineering, Newcastle University, Newcastle upon Tyne, GB |
| 19:00  | FM01.1.7| High-level Constructive Synthesis of Domain-Specific Kernels of Cryptography  
Speaker: Ayesha Kaidi, RWTH Aachen, DE |
| 19:00  | FM01.1.8| Safety-Assured Model-Based Implementation of the GPCA Infusion Pump Software  
Speaker: |
19:00 FM01.1.9  Improved Test Techniques for Network-on-Chip Based Memory Systems  
Speaker: Bibhas Ghoshal, IIT, Kharagpur, IN

19:00 FM01.1.10  Dedicated Hardware Accelerators for High Efficiency Video Coding Standard  
Speaker: Cláudio Diniz, UFRGS, BR

19:00 FM01.1.11  Heat Dissipation and Thermal Analysis for 3D ICs  
Speaker: Cristiano Santos, PGMICRO-UFRGS / CEA-Leti, BR

19:00 FM01.1.12  Fault Tolerance for Real-Time Systems: Analysis and Optimization of Roll-back Recovery with Checkpointing  
Speaker: Dimitar Nikolov, Lund University, Department of Electrical and Information Technology, SE

19:00 FM01.1.13  SynthHorus2: A Tool for Assertion-based Synthesis  
Speaker: Fatemeh JAVAHERI, TIMA Lab, FR

19:00 FM01.1.14  Multilevel Modeling, Formal Analysis, and Characterization of Soft Errors in Digital Systems  
Speaker: Ghaith Bany Hamad, Polytechnique Montréal, CA

19:00 FM01.1.15  RHetOS: A Reconfigurable and Heterogeneous Operating System  
Speaker: Gianluca Durelli, Politecnico di Milano, IT

19:00 FM01.1.16  Path-Based Program Repair  
Speaker: Heinz Riener, University of Bremen, DE

19:00 FM01.1.17  Theory and applications of quantum process calculus  
Speaker: Ittoop Puthoor, University of Glasgow, GB

19:00 FM01.1.18  Non-Invasive Extraction of SystemC Meta Data  
Speaker: Jannis Stoppe, DFKI GmbH, DE

19:00 FM01.1.19  Synthesis and Optimization of Reversible Logic Circuits  
Speaker: Kamaïka Datta, National Institute of Technology, Meghalaya, IN

19:00 FM01.1.20  Optical Interconnects for Computing Systems: a Formal Study on Signal-to-Noise Ratio  
Speaker: Mahdi Nikdast, Polytechnique Montréal, CA

19:00 FM01.1.21  NoC-Centric Partitioning and Reconfiguration Technologies for the Efficient Sharing of Many-Core Platforms  
Speaker: Marco Balboni, University of Ferrara, IT

19:00 FM01.1.22  Post-Manufacturing One-Shot Calibration of RF Circuits Based on Non-Intrusive Sensors  
Speaker: Martin Andraud, TIMA Laboratory, FR

19:00 FM01.1.23  Range Based Analysis of Inner Systems Characteristics  
Speaker: Michael Rathmair, Institute of Computer Technology, Vienna University of Technology, AT

19:00 FM01.1.24  Making Homogeneous the Platform-Based Design of Heterogeneous Cyber-Physical Systems  
Speaker: Michele Lora, University of Verona, IT

19:00 FM01.1.25  Power Aware Test and Test of Low Power Devices  
Speaker: Miroslav Valka, Post-Doc, FR
19:00  FM01.1.26  Run-Time Resource Management of Networked Many-Core Systems  
Speaker:  
Mohammad Fattah, University of Turku, FI

19:00  FM01.1.27  Thermal Management Using Thermoelectric Coolers  
Speaker:  
Mohammad Javad Dousti, University of Southern California, US

19:00  FM01.1.28  Yield and Cost Analysis for 3D Stacked ICs  
Speaker:  
Mottaqiallah Taouil, Delft University of Technology, NL

19:00  FM01.1.29  Hardware-Software Co-Design for Next Generation Dark Silicon Multimedia Systems  
Speaker:  
Muhammad Usman Karim Khan, Karlsruhe Institute of Technology (KIT), DE

19:00  FM01.1.30  Design Space Exploration of Thread vs. Arithmetic Level Parallelism to Design a Computation Unit of the Multi-chip eBrain Supercomputer  
Speaker:  
Nasim Farahini, KTH, SE

19:00  FM01.1.31  Towards High Performance and Efficiency of Heterogeneous Systems  
Speaker:  
Sam Skalicky, Rochester Institute of Technology, US

19:00  FM01.1.32  Power: Its Manifestations in Digital Systems Testing  
Speaker:  
Seetal Potluri, Indian Institute of Technology Madras, IN

19:00  FM01.1.33  Managing the Complexity in Embedded and Cyber-Physical System Design - System Modeling and Design-Space Exploration  
Speaker:  
Seyed-Hosein Attarzadeh-Niaki, KTH Royal Institute of Technology, SE

19:00  FM01.1.34  Logic Rewiring: a Practical Bridging Technique between VLSI Logical and Physical Syntheses  
Speaker:  
Xing Wei, Chinese University of Hong Kong, HK

19:00  FM01.1.35  Distributed Thermal Management for Large-Scale Chip-Multiprocessors  
Speaker:  
Yingnan Cui, Nanyang Technological University, SG

19:00  FM01.1.36  High-level Modeling, Estimation and Exploration of Reliability for MPSoC  
Speaker:  
Zheng Wang, RWTH-Aachen University, DE

19:00  FM01.1.37  Modeling, Analysis and Exploration of Layers: A 3D Computing Architecture  
Speaker:  
Zoltán Endre Rákossy, Institute for Communication Technologies and Embedded Systems (ICE), RWTH Aachen, DE

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