



**Conference and Exhibition: March 8-12, 2010
Dresden, Germany**

Call for Papers

Scope of the Event

The 13th DATE conference and exhibition is the main European event bringing together designers and design automation users, researchers and vendors, as well as specialists in the hardware and software design, test and manufacturing of electronic circuits and systems. It puts strong emphasis on both ICs/SoCs, reconfigurable hardware and embedded systems, including embedded software.

Structure of the Event

The five-day event consists of a conference with plenary invited papers, regular papers, panels, hot-topic sessions, tutorials, workshops, two special focus days and a track for executives. The scientific conference is complemented by a commercial exhibition showing the state-of-the-art in design and test tools, methodologies, IP and design services, reconfigurable and other hardware platforms, embedded software, and (industrial) design experiences from different application domains, such as automotive, wireless, telecom and multimedia applications. The organisation of user group meetings, fringe meetings, a university booth, a PhD forum, vendor presentations and social events offers a wide variety of extra opportunities to meet and exchange information on relevant issues for the design and test community. Special space will also be allocated for EU-funded projects to show their results. More details are given on the DATE website (www.date-conference.com).

Areas of Interest

Within the scope of the conference, the main areas of interest are: embedded systems, design methodologies, CAD languages, algorithms and tools, testing of electronic circuits and systems, embedded software, applications design and industrial design experiences. Topics of interest include, but are not restricted to:

- System Design Methods, Algorithm and Tools
- System Level Specification and Modeling
- System Synthesis and Optimization
- Model-based Design and Test
- Multi-Processor and Multi-Core Systems
- Network on Chip
- Reconfigurable and Adaptive Systems and Computing
- Real-time, Networked and Dependable Embedded Systems
- Simulation and Validation
- Assertion-based and Coverage-driven Verification
- Formal Methods and Formal Verification
- Design of Low Power Systems
- Power Estimation, Verification and Optimization
- Architectural and Microarchitectural Design
- Architectural Synthesis
- Logic and Technology Dependent Synthesis for Deep-Submicron Circuits
- Physical Design and Verification
- Analogue and Mixed Signal Systems and Design
- Interconnect, EMC and Packaging
- MEMS and Nanotechnology
- Sensor Networks and Technologies
- Compilers and Code Generation for Embedded Systems
- Software for MPSoC and Multi-Core Systems
- Hardware-Dependent Software
- System and Industrial Test
- Test and Design for Manufacturability
- Design for Test
- BIST and Embedded Test
- Test Generation, Simulation and Diagnosis
- On-line Testing and Fault Tolerance
- High-Speed Test
- Test for Variability, Reliability and Defects
- Analogue/Mixed-Signal/MEMS/RF Testing
- Industrial Design Methods
- Media and Signal Processing
- Automotive Electronics and Software
- Wireless Communication and Networking
- Secure and Safety-Critical Implementations
- Military, Space and Avionics
- Technologies and Applications for Healthcare
- Emerging Technologies, Systems, Standards and Applications
- Design and Application Case Studies

Submission of Papers

All papers have to be submitted electronically by September 6th, 2009 via:

<http://www.date-conference.com/submit.html>

Papers can be submitted either for standard oral presentation or for interactive presentation.

Conference and Exhibition Secretariat

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