

technical programme topic chairs

System Specification and Modeling

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Universidad de Cantabria, ES
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MPSoC and System Design Methods

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System Synthesis and Optimisation

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Paul Pop
TU of Denmark, DK

Simulation and Validation

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U of Verona, IT
Ian Harris
U of California Irvine, US

Design of Low Power Systems

Miguel Miranda
IMEC, BE
Alberto Macii
Politecnico di Torino, IT

Power Estimation and Optimisation

Dimitrios Soudris
U of Thrace, GR
Kaushik Roy
Purdue U, US

Innovative and Emerging Technologies, Systems and Applications

Marc Geilen
TU Eindhoven, NL
Carlotta Guiducci
U di Bologna, IT

Formal Verification

Andreas Kuehlman
Cadence, US
Roderick Bloem
TU Graz, AT

Network on Chip

Axel Jantsch
KTH, SE
Li-Shiuan Peh
Princeton U, US

Microarchitectural and Architectural Design

Todd Austin
U of Michigan, US
Dionisios Pneumatikatos
TU of Crete, GR

Architectural Synthesis

Fabrizio Ferrandi
Politecnico di Milano, IT
Ryan Kastner
UC Santa Barbara, US

Reconfigurable Computing

Wayne Luk
Imperial College, UK
Juergen Becker
U of Karlsruhe (TH), DE

Logic and Technology Dependent Synthesis for Deep-Submicron Circuits

Tiziano Villa
U of Verona, IT
Steven Nowick
U of Columbia, US

Physical Design and Verification

Patrick Groeneveld
Magma, NL
Jens Lienig
TU Dresden, DE

Analogue and Mixed A/D Systems

Manfred Glesner
TU Darmstadt, DE
Alex Doboli
Stony Brook U, US

CAD for Analogue and Mixed Signal Design, Symbolic Techniques

Tom Kazmierski
U of Southampton, UK
Helmut Graeb
TU Munich, DE

Interconnect, EMC and Packaging Modelling

Luis Miguel Silveira
TU Lisbon, PT
Flavio Canavero
Politecnico di Torino, IT

Media and Signal Processing

John Dielissen
NXP Semiconductor, NL
Ian Phillips
ARM, UK

Wireless Communication and Networking

Cyprian Grassmann
Infineon, DE
Guido Maserà
Politecnico di Torino, IT

Automotive

Luca Fanucci
Pisa U, IT
Annette Reutter
Bosch, DE

Secure and Security Systems

Olivier Deprez
Texas Instruments, FR
Jerome Quevremont
Thales, FR

Space and Avionics Systems

Bernard Candaele
Thales, FR
Philippe Manet
UCL U, Louvain-La-Neuve, BE

Application of Reconfigurable and Adaptive Systems

Christoph Heer
Infineon Technologies, DE
Michael Huebner
U of Karlsruhe, DE

MEMS and Sensor Systems

Alessandro Rocchi
Sensordynamics, AT
Jorg Eichholz
Fraunhofer ISIT, DE

Multi-Core Platforms

Marcello Coppola
STMicroelectronics, FR
Frederic Petrot
TIMA Laboratory, FR

Implementation of Analogue and Mixed Signal Systems

Geert Van der Plas
IMEC, BE
Paolo D'Abamo
Austriamicrosystems, IT

System and Industrial Test

Erik Larsson
U of Linkoping, SE
Peter Harrod
ARM, UK

Design for Test and BIST

Krishnendu Chakrabarty
Duke U, US
Paolo Prinetto
Politecnico di Torino, IT

Test Generation, Simulation and Diagnosis

Bernd Becker
U of Freiburg, DE
Bernd Straube
FHG IIS/EAS, DE

On-Line Testing, Fault Tolerance, and Reliability

Dhiraj Pradhan
U of Bristol, UK
Davide Appello
STMicroelectronics, IT

Testing of Analogue, Mixed-Signal, RF and Heterogeneous Circuits and Systems

Salvador Mir
TIMA Laboratory, FR
Hans Kerkhoff
U of Twente, NL

Statistical, Physical, Defect-Based Testing and Test of Regular Structures

Antonio Rubio
U Politecnica Catalonia, ES
Hans Manhaeve
Q-Star Test, BE

Real-Time and Dependable Systems - Principles and Practice

Petru Eles
U of Linkoping, SE
Steve Goddard
U of Nebraska-Lincoln, US

Compilers and Code Generation for Embedded Systems

Shuvra Bhattacharyya
U of Maryland, US
Rainer Leupers
RWTH Aachen U, DE

Model-Based Design for Embedded Systems

Ed Brinksma
TU Eindhoven, NL
Pieter Mosterman
The MathWorks, US

Software Architectures and Principles for Embedded MPSoC and Multi-core Systems

Chris Schlaeger
AMD, Dresden, DE
Pascal Felber
U of Neuchâtel, CH

Software Architectures and Principles for Networked Embedded Systems

Michael Beigl
TU Braunschweig, DE
George Pappas
U of Pennsylvania, US

Special Topic: Dependable Embedded Systems

Neeraj Suri
TU Darmstadt, DE
Christof Fetzer
TU Dresden, DE

Special Topic: Automotive Electronics

Alberto Sangiovanni Vincentelli
UC Berkeley, US
Marco Di Natale
Scuola S Anna, Pisa, IT