

Foreword

On behalf of the Executive and Technical Programme Committees, we take great pleasure in welcoming you to Munich and to **DATE 05**, the most comprehensive European Conference and Exhibition event bringing together academic researchers, industry specialists, users and vendors in the fields of Design, Automation and Test of electronic circuits and embedded systems.

The endless quest for faster, less consuming, cheaper and safer electronic products, particularly for the growing consumer and communications markets, dictate that increasingly complex designs be generated in continuously shrinking time scales. Design automation is a strategic technology for modern electronic systems, from simple ASICs via embedded IP cores to large systems on chip made of heterogeneous processors communicating via on-chip networks. The use of embedded processors in SoCs requires an ever-increasing emphasis on embedded software development techniques as part of the design process of state-of-the-art electronic systems. Testing of these complex electronic systems is becoming a key factor in enabling the final quality goals and has a growing impact in the overall cost of product development.

DATE has become the pre-eminent European conference addressing research and development activities in the field of design technology and is more and more moving to a system design event focusing on common platform challenges for embedded systems. World leading companies in multimedia, wireless communications, and automotive industries have led to the emergence of recognised European strengths in System-on-Chip Technologies, particularly Embedded Systems with a strong analogue and mixed-signal content. **DATE** is a unique event where the latest scientific and technological developments are brought to the public from the leading players in the field. **DATE**, therefore, offers a once-in-a-year occasion to find out more about the technical and architectural trends of circuits and systems, small and large, and the design automation and test challenges they bring to users, tool providers and PCB companies as well as semiconductor and IP providers alike.

A total of 825 submissions were received for **DATE 05**, again an increase over the previous edition. After careful consideration, the Programme Committee has selected 176 papers for regular oral presentation in 54 sessions — an acceptance rate of 25 percent — and 58 papers as interactive presentations (a new format started at **DATE 04** to present novel ideas of “ongoing work” of high quality). An additional 45 regular papers and 6 interactive presentations will be presented in the Designers’ Forum, a technical programme track devoted to the specific needs of designers. Thirteen Special Sessions selected out of 29 submissions have been chosen to bring stimulating insights and vibrant discussions in hot topics. The main technical programme is composed of seven tracks running in parallel. This year, for the first time, a complete embedded software track is offered to allow for the increasing importance of software in embedded systems. In complement to the main technical and scientific programme, we have an educational day on the day before the conference. This day is composed of tutorials and master courses. The day after the conference is dedicated to five workshops. The organisation of a PCB Symposium, a University Booth, fringe meetings and social events during the conference offer a wide variety of extra opportunities to meet and exchange information on relevant issues for the design, automation and test communities.

DATE continues its tradition of grouping presentations and activities related to Special Themes on specific focus days. This year’s Special Days are dedicated to AUTOMOTIVE (Wednesday), a real European strength and BIOCHIPS (Thursday), a new emerging technology. In addition to the two keynote addresses presented at the Tuesday Opening Plenary we will have two keynotes related to these Special Days.

The Executive Track, initiated for the first time last year, is designed to offer the views of high-level executives from EDA and semiconductor and system houses. Presentations by company CEOs, CTOs, VPs and other senior executives will take place on Tuesday, March 8th.

The Friday Workshops started at **DATE 04** offer the possibility to attend Workshops as a complement to the regular conference running throughout the week. Five Workshops will run in parallel allowing in-depth discussions of specific topics or new technical areas which are not directly covered by the technical programme.

Following its successful implementation at **DATE 03** the Focus on Business and Industry, a presentation theatre located on the exhibition floor, offers exhibition visitors and conference delegates a fresh view on economics, European strength and challenges in the current electronic systems design market.

Many volunteers have given their best efforts to make this Conference and Exhibition an outstanding event. We would like to thank all the members of the Sponsors Committee, the Executive Committee, the Technical Programme Committee, the Vendors Committee and the Exhibitors, and specially our recognition goes to all the Authors, Speakers, Session Organisers, Session Chairs, and Reviewers for their continued interest, energy and support to **DATE**. We hope that you will enjoy the **DATE 05** Conference and Exhibition as much as all of us have enjoyed organising it for you.

Norbert Wehn
General Chairman



Luca Benini
Program Chairman

