

Best Paper Awards

Each year the Design, Automation and Test in Europe Conference presents awards to the authors of the most outstanding papers of the previous year's conference. The selection is based on the results of the reviewing process and the voting by the conference participants.

The paper selected as the most outstanding in the field of CAD (track A) is:

Techniques for Reducing Read Latency of Core Bus Wrappers

*by Roman L. Lysecki, Frank Vahid, and Tony D. Givargis
of the University of California, Riverside, USA*

The authors address the problem of assembling cores in system-on-a-chips, without introducing extra latencies. They present a technique for automatically designing a pre-fetch unit that satisfies user-imposed register-access constraints, and extend this technique to allow user-specified register interdependencies.

The paper selected as the most outstanding in the field of Tools (track B) is:

Code Selection for Media Processors with SIMD Instructions

by Rainer Leupers of the University of Dortmund, Germany

The author presents a novel code selection technique capable of exploiting SIMD instructions when compiling plain C source code. This technique takes advantage of SIMD instructions for multimedia applications, while still using portable source code.

The paper selected as the most outstanding in the field of Test (track C) is:

Cost Reduction and Evaluation of a Temporary Faults Detecting Technique

by Lorena Anghel, and Michael Nicolaidis of TIMA, France.

The authors consider the implementation of a new soft error and timing error detecting technique based on time redundancy. Results obtained by simulations and performance evaluation on arithmetic circuits show that the detection of such faults can be achieved by means of meaningful hardware and performance cost.

Congratulations to the winners!