

# Program Topic Co-Chairs



SYSTEM DESIGN  
**Flavio Wagner**  
UFRGS, BRZ



ANALOGUE  
**Georges Gielen**  
KU Leuven, B



LOW POWER  
**Jacques Benkoski**  
Momterey Design Systems,  
USA



DESIGN RE-USE  
**Ralf Seepold**  
FZI Karlsruhe, D



CASE STUDIES  
**Klaus Kronlöf**  
Nokia, FIN



FSM SYNTHESIS  
**Michel Berkelaar**  
Eindhoven University of  
Technology, NL



FORMAL VERIFICATION  
**Paolo Camurati**  
Politecnico di Torino, IT



HDL USE  
(E)  
**Wolfgang Nebel**  
University of Oldenburg, D



PHYSICAL DESIGN  
(E)  
**Toshiro Akino**  
Kinki U, J



CODESIGN  
**Ahmed Jerraya**  
TIMA, Grenoble, F



ARCHITECTURAL  
SYNTHESIS  
**Fadi Kurdahi**  
University of California  
Irvine, USA



SIMULATION  
**Peter Schwarz**  
FhG IIS/EAS Dresden  
D



DESIGN LANGUAGES  
**Eugenio Villar**  
Cantabria U, ES



TEST GENERATION  
**Ben Bennetts**  
Bennetts Associates, UK



DESIGN FOR TESTABILITY  
**Einar Aas**  
Trondheim U, N



EMBEDDED TEST  
**Yervant Zorian**  
LogicVision , USA